

Docket No. 241160US2S  
IN RE APPLICATION OF: Yoshinori MATSUBARA  
SERIAL NO: 10/632,822  
FILED: August 4, 2003  
FOR: SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE MANUFACTURING METHOD, AND  
SEMICONDUCTOR DEVICE TEST METHOD



AF/IDW  
"RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2.8/5"

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment under 37 C.F.R. § 1.116 in the above-identified application.

- ☐ No additional fee is required  
☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.  
☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	4	MINUS	26	0	x \$50 =	\$0.00
INDEPENDENT	4	MINUS	3	1	x \$200 =	\$200.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$360 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$200.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$200.00

- ☐ A check in the amount of **\$0.00** is attached.
- ☒ Credit card payment form is attached to cover the fees in the amount of **\$200.00**
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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"RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2815"

DOCKET NO: 241160US2S

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
YOSHINORI MATSUBARA : EXAMINER: CLARK, S. V.  
SERIAL NO: 10/632,822 :  
FILED: AUGUST 4, 2003, : GROUP ART UNIT: 2815  
FOR: SEMICONDUCTOR DEVICE, :  
SEMICONDUCTOR DEVICE  
MANUFACTURING METHOD,  
AND SEMICONDUCTOR DEVICE  
TEST METHOD

AMENDMENT UNDER 37 C.F.R. § 1.116

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated May 3, 2005, please amend the above-  
identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on  
page 2 of this paper.

**Remarks** begin on page 5 of this paper.

07/14/2005 JADD01 00000016 10632822

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